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We give a new algorithm for generating Greechie diagrams with arbitrary chosen number of atoms or blocks (with 2, 3, 4, ... atoms) and provide a computer program for generating the diagrams. The results show that the previous algorithm does not produce every diagram and that it is at least 10^5 times slower. We also provide an algorithm and programs for checking Greechie diagram passage by equations defining varieties of orthomodular lattices and give examples from Hilbert lattices. We also discuss some additional characteristics of Greechie diagrams.

1. INTRODUCTION

To arrive at a Hilbert space representation of measurement starting from plausible "physical" axioms has been a dream of many physicists and mathematicians for almost 70 years. Of course, one could not expect to recognize the axioms from nothing but experimental data because they, provided they exist, must be rather involved. Therefore scientists took the opposite road by starting with Hilbert space and trying to read off essential mathematical properties so as to be able eventually to simplify them and arrive at simple physically plausible axioms.

The first breakthrough along this opposite road was made by Birkhoff and von Neumann in 1936 [1], who recognized that a modular lattice which

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can be given a physical background underlies every finite-dimensional Hilbert space. In the early sixties Mackey [2] (and Zierler [3]) arrived at six axioms for a poset (partially ordered set) of physical observables which he essentially *read off* from the Hilbert space properties. In an additional famous *seventh* axiom, he then *postulated* that the latter poset be isomorphic to the one of the subspaces of an infinite-dimensional Hilbert space. A few years later Piron [4], MacLaren [5], Amemyia and Araki [6], and Mączyński [7], starting with such a poset with infima and suprema of every two-element subset (lattice) and using similar axioms, proved that the lattice, usually called the *Hilbert lattice*, is isomorphic to a pre-Hilbert space. That enabled Mączyński [7] to postulate only the kind of field over which the Hilbert space should be formulated: he chose the complex one. The Hilbert lattice is then a lattice of the subspaces of the Hilbert space.

At that time it seemed that only two other fields could have been postulated: the real and the quaternionic ones. But in the early eighties Keller [8] showed that there are other nonstandard (non-Archimedean) fields over which a Hilbert space can be defined. Also, the axioms themselves proved to be too complicated to be given plausible physical support or simplified. Thus, the whole project lost its appeal and the majority of researchers left the field. However, in 1995 Solèr [9] proved that an infinite-dimensional Hilbert space can only be defined over either a real, a complex, or a quaternionic field (i.e., that only finite-dimensional ones allow nonstandard fields).

The latter result renewed interest in the problem of reconstructing the Hilbert space from an algebra of observables [10-15] Also, recently devised quantum computers call for such a reconstruction from an algebra which in the field of quantum computing is usually called quantum logic in analogy to the classical logic underlying classical computers. In particular, if we wanted quantum computers to function as quantum simulators, i.e., directly to simulate quantum systems through their description in the Hilbert space, we apparently have to start from such an algebra. For, this would be the only presently conceivable way of typing in the Hamiltonian at the console of the quantum simulator. This also means that we have to go around the present standard axioms for the Hilbert lattice not because they are complicated and physically nongrounded, but because they include universal and existential quantifiers which are unmanageable by a quantum computer. A way to do so would be to find lattice equations as substitutes for the axioms. The Hilbert lattice satisfies not only the orthomodularity equation, but a number of other equations as well. Thus, if we started with such a lattice, which can easily be physically supported by, e.g., a quantum computer design, we could obviously simplify the axioms and possibly ultimately dispense with them. The problem is that only two groups of equations

satisfied by Hilbert lattices, i.e., in any Hilbert space, have been found so far. We do not know whether the Hilbert space equations form a recursively enumerable set, i.e., whether we can determine them all. What we can do, however, is to try to find as many such equations as possible and group them according to their recursive algorithms. Each such equation can simplify the present axioms.

However, since already equations with four variables contain at least about 30 terms which one cannot further simplify, a proper tool for finding and handling the equations is indispensable. A great help are Greechie diagrams (condensed Hasse diagrams), which we will define precisely later. To show that two equations cannot be inferred from each other it suffices to find two Greechie lattices in which the equations interchangeably pass and fail. As an illustration of how "easily" one can find a lattice without a computer program we cite Greechie: "[In 1969] a student, beginning his dissertation, found such a lattice. It was terribly complicated and had about eighty atoms. The student left school and the example was lost. I've been looking for one ever since. Recently [in 1977!] I found one" [16]. So, we need an algorithm for finding Greechie diagrams and another for finding whether a particular equation passes or fails them. In this paper we give both. They would not only support the aforementioned project of obtaining the Hilbert space from physically plausible axioms, but would also serve for obtaining new equations in the theory of Hilbert spaces.

The first attempt at automated generation of Greechie diagrams was made in the early eighties by Beuttenmüller, a former student of G. Kalmbach [17, pp. 319–328]. The algorithm itself is not given in the book, but G. Beuttenmüller kindly sent us the listing of its translation into Algol. We rewrote it in C, and with a fast PC it took about 27 days to generate Greechie diagrams with 13 blocks. We estimated it would take around a year for 14 blocks and half a century for 15 blocks, so we looked for another approach.

The technique of *isomorph-free exhaustive generation* [18] of Greechie diagrams gave us not only a tremendous speed gain—48 sec; 6 min; 51 min; 8 h and 122 h for 13–17 blocks, respectively (for a PC running at 800 MHz)—but also essentially new results: Beuttenmüller's algorithm must be at least incomplete since the numbers of nonisomorphic Greechie diagrams in Kalmbach's book ([17], p. 322) are wrong. In Section 2 we give the algorithm for the above generation.

In Section 3 we give an algorithm for checking whether a particular equation fails or passes in Greechie diagrams provided by the algorithm from Section 2. The algorithm has helped us to find new equations that hold in any infinite-dimensional Hilbert space [19].

2. ISOMORPH-FREE EXHAUSTIVE GENERATION OF GREECHIE DIAGRAMS

The following definitions and theorem are taken from Kalmbach [17] and Svozil and Tkadlec [20]. Definitions in the framework of *quantum logics* (σ -orthomodular posets) can be found in the book by Pták and Pulmannová [21].

Definition 2.1. A diagram is a pair (V, E), where $V \neq \emptyset$ is a set of atoms (drawn as points) and $E \subseteq \exp V \setminus \{\emptyset\}$ is a set of blocks (drawn as line segments connecting corresponding points). A loop of order $n \ge 2$ (*n* being a natural number) in a diagram (V, E) is a sequence $(e_1, \ldots, e_b) \in E^n$ of mutually different blocks such that there are mutually distinct atoms v_1, \ldots, v_n with $v_i \in e_i \cap e_{i+1}$ $(i = 1, \ldots, n, e_{n+1} = e_1)$.

Definition 2.2. A *Greechie diagram* is a diagram satisfying the following conditions:

- 1. Every atom belongs to at least one block.
- 2. If there are at least two atoms, then every block is at least 2-element.
- 3. Every block which intersects with another block is at least 3-element.
- 4. Every pair of different blocks intersects in at most one atom.
- 5. There is no loop of order 3.

Theorem 2.3. For every Greechie diagram with only finite blocks there is exactly one (up to an isomorphism) orthomodular poset such that there are one-to-one correspondences between atoms and atoms and between blocks and blocks which preserve incidence relations. The poset is a lattice if and only if the Greechie diagram has no loops of order 4.

In the literature, a block is also called an *edge* and an atom is also called a *vertex* or *node*. (However, we reserve the term *node* for an element of a Hasse diagram.)

From the above definitions it is clear that a block can have not only 3 atoms, but also 2 or 4 or more atoms. However, practically all examples of Greechie diagrams used in lattice theory are nothing but pasted 3-atom blocks. We are aware of only two important contributions containing 4-atom blocks (two proofs of the existence of finite lattices admitting no states given in ref. 21, Fig. 2.4.5, p. 37, and ref. 17, Fig. 17.3, p. 275) and of only one result for *n* and ∞ giving orthomodular lattices without states [17, Fig. 17.4, p. 275].

For this reason, we have initially focused on generation of diagrams with every block having size 3. Nevertheless, our description of the generation algorithm will allow larger blocks in anticipation of the next version of our generation program. Our program for checking equations already handles large blocks. Also, we are interested only in the diagrams which correspond to lattices, i.e., only in those containing no loops of order 4. Since Condition 5 of Definition 2.2 states that there are no loops of order 3, this means that we are interested only in diagrams with loops of order 5 and higher. Those 3-atom Greechie diagrams which correspond to lattices we call *Greechie-3-L diagrams*.

A diagram is *connected* if, for each pair of atoms v, v', there is a sequence of blocks e_1, e_2, \ldots, e_k such that $v \in e_1, v' \in e_k$, and $e_i \cap e_{i+1} \neq \emptyset$ for $1 \leq i \leq k - 1$. In Section 3 we will illustrate how the properties of unconnected diagrams are not necessarily a simple combination of the properties of their connected components, so our algorithms will handle both connected and unconnected diagrams. An *isomorphism* from a diagram (V_1, E_1) to a diagram (V_2, E_2) is a bijection ϕ from V_1 to V_2 such that ϕ induces a bijection from E_1 to E_2 . The isomorphisms from a diagram D to itself are its *automorphisms*, and together comprise its *automorphism group* Aut(D).

If D = (V, E) is a diagram and $e \in E$, then D - e is the diagram obtained from D by removing e and also removing any atoms that were in e, but in no other block. Conversely, if e is a set of atoms (not necessarily all of them atoms of D), then D + e is the diagram $(V \cup e, E \cup \{e\})$. Clearly, (D + e) - e = D.

We will describe the generation algorithm in some generality to assist future applications. Suppose that \mathscr{C} is some class of diagrams closed under isomorphisms (for example, connected Greechie-3-L diagrams). If $D = (V, E) \in \mathscr{C}$ and |E| > 1, there may be some $e \in E$ such that $D - e \in \mathscr{C}$. If there is no such block, we can *D* irreducible. It is obvious that all diagrams in \mathscr{C} can be made from the irreducible diagrams in \mathscr{C} by adding a sequence of blocks one at a time, all the while staying in \mathscr{C} . Such a sequence of diagrams is a construction path for *D*.

The basic idea behind our algorithm is to prune the set of construction paths until (up to isomorphism) each diagram in \mathscr{C} has exactly one construction path. This is achieved by two techniques acting in consort.

The first technique is to avoid equivalent extensions. Suppose $D \in \mathcal{C}$ and e_1 and e_2 are such that $D + e_1$ and $D + e_2 \in \mathcal{C}$. Then $D + e_1$ and $D + e_2$ are called *equivalent extensions* of D if $|e_1| = |e_2|$ and there is an automorphism of D which maps $V \cap e_1$ onto $V \cap e_2$. It is easy to see that the equivalence of e_1 and e_2 implies the isomorphism of $D + e_1$ and $D + e_2$, by an isomorphism that takes e_1 onto e_2 , so we do not lose any isomorphism types of diagram if we make only one of them.

The second technique is somewhat more complicated. Suppose we have a function m with the following properties.

- m(·) takes a single argument D which is a reducible diagram in C. It returns a value which is an orbit of blocks under the action of Aut(D).
- $D e \in \mathscr{C}$ for every $e \in m(D)$.
- If D' is a diagram isomorphic to D, then there is an isomorphism from D to D' that maps m(D) onto m(D').

We will explain how to compute such a function $m(\cdot)$ later; for now we will describe its purpose. Take any reducible $D \in \mathcal{C}$ and $e \in m(D)$; then form D - e. If we do the same starting with a diagram D' isomorphic to D—take $e' \in m(D')$, then form D' - e'—the third property of $m(\cdot)$ implies that D - e and D' - e' are isomorphic. Thus, the function $m(\cdot)$ enables us to define a unique isomorphism class, that of D - e for $e \in m(D)$, as the *parent class* of the isomorphism class of D. Since we wish to avoid making isomorphism types more than once, we can decide to only make each diagram from its parent class. If we happen to make it from any other class, we will reject it.

The result of the theory in ref. 18 is that the combination of the above two techniques results in each isomorphism class being generated exactly once. The precise method of combination is given by the following algorithm.

Definition 2.4. Isomorph-free Greechie diagram generation procedure:

```
procedure scan (D: diagram; \beta: integer)

if D has exactly \beta blocks then

output D

else

for each equivalence class of extensions D + e do
```

```
for each equivalence class of extensions D + e
if e \in m(D + e) then scan(D + e, \beta)
end procedure
```

In Fig. 1 we show the top four levels of the generation tree as produced by our implementation of the algorithm for connected Greechie-3-L diagrams. The lines joining the diagrams show the parent-child relationship. Between a parent and its child, one block is added. Note that diagram $D_{4,3}$ is made by adding a block to $D_{3,1}$, but it could also be made by adding a block to $D_{3,2}$. The reason that $D_{3,1}$ is its real parent is that $m(D_{4,3})$ consists of the upper right and lower right blocks (which are equivalent) of $D_{4,3}$. (This is a fact of our implementation which cannot be seen by looking at the figure.) When $D_{4,3}$ is made from $D_{3,1}$, the new edge is seen to be in $m(D_{4,3})$ and so the diagram is accepted. When it is made from $D_{3,2}$, the new edge is found to be not in $m(D_{4,3})$ and so the diagram is rejected. The idea is that each (isomorphism type of) diagram is accepted exactly once, no matter how many times it is made. This is proved in the following theorem.

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Fig. 1. Generation tree for connected Greechie-3-L diagrams.

Theorem 2.5. Suppose we call scan(D, β) for one D from each isomorphism class of irreducible diagram in \mathscr{C} that has at most β blocks. Then the output will consist of one diagram from each isomorphism class in \mathscr{C} with exactly β blocks.

Proof. The theorem is a special case of one in ref. 18, and the reader is referred to that paper for a strictly formal proof. Here we will give a slightly less formal sketch.

Let us say that a diagram *D* is *accepted* by the algorithm if a call *scan*(*D*, β) occurs. We will first prove that *at least* one member of each isomorphism class of diagram in \mathscr{C} with at most β blocks is accepted. Then we will prove that *at most* one member of each isomorphism class is accepted. These two facts together will obviously imply the truth of the theorem.

Suppose that the first assertion is false: there is an isomorphism class in \mathscr{C} , with at most β blocks, that is never accepted. Let *D* be a member of such a missing isomorphism class which has the least number of blocks. *D* cannot be irreducible, since all irreducible diagrams are accepted explicitly. Thus, we can choose $e \in m(D)$ and consider D - e. Since $D - e \in \mathscr{C}$ and D - e has fewer blocks than *D*, at least one isomorph *D'* of D - e is accepted.

The isomorphism from D - e to D' maps $V(D) \cap e$ onto some subset of V(D'). Let e' be a set of atoms consisting of that subset plus enough new atoms to make e' the same size as e. The **for** loop considers some extension D' + e'' equivalent to D' + e', since it considers all equivalence classes of extensions. Moreover, since $e \in m(D)$ we can infer that $e' \in m(D' + e')$ and consequently that $e'' \in m(D' + e'')$. This means that the algorithm will perform the call $scan(D' + e'', \beta)$, which is a contradiction, as D' + e'' is isomorphic to D and the isomorphism class of D was supposed to be not accepted at all. This proves that all isomorphism classes are accepted at least once.

Next suppose that some isomorphism type is accepted twice. Namely, there are two isomorphic but distinct diagrams D and D' in \mathcal{C} , with at most M blocks, such that both D and D' are accepted. Choose such a pair D, D' with the least number of blocks.

As before, D and D' cannot be irreducible, so they must be accepted by some calls $scan((D - e) + e, \beta)$ and $scan((D' - e') + e', \beta)$ which arise from the calls $scan(D - e, \beta)$ and $scan(D' - e', \beta)$, respectively, where $e \in m(D)$ and $e' \in m(D')$. The properties of $m(\cdot)$ ensure that D - e and D' - e' are isomorphic, so they must in fact be the same diagram D'' (since isomorphism classes with fewer blocks than D are accepted at most once by assumption). However, D'' + e and D'' + e' are equivalent, but distinct extensions of D'', which violates the **for** loop specification. This contradiction completes the proof.

The success of the algorithm requires us to be able to find the irreducible diagrams in \mathscr{C} by some other method, but in many important cases this is easy. We give the most important example.

Theorem 2.6. Suppose \mathscr{C} is a class of Greechie diagrams defined by some fixed set of permissible block sizes, some fixed set of permissible loop lengths, and an optional restriction to connected diagrams. Then the only irreducible diagrams in \mathscr{C} are those with one block.

Proof. Consider a diagram $D \in \mathcal{C}$ with more than one block.

If \mathscr{C} is not restricted to connected diagrams, $D - e \in \mathscr{C}$ for any $e \in E(D)$, so D is reducible.

Suppose instead that \mathscr{C} contains only connected diagrams. Choose a longest possible sequence *S* of distinct blocks e_1, e_2, \ldots, e_k , where $e_i \cap e_{i+1} \neq \emptyset$ for $1 \leq i \leq k - 1$. Let v_1 and v_2 be two atoms of $D - e_k$. Since *D* is connected, there is a chain of blocks from v_1 to v_2 . This same chain is in $D - e_m$ unless it contains e_k . However, all the blocks *D* intersecting e_k are in *S* (or else *S* can be made longer), so e_k can be replaced in *S* by some portion of *S*. Hence $D - e_k$ is connected, so *D* is reducible.

The correctness of the algorithm does not depend on the definition of $m(\cdot)$ provided it has the properties we required of it. The actual definition of $m(\cdot)$ used in our program is carefully tuned for optimal observed performance, and is too complicated to describe here in detail, but we will outline a simpler definition that is the same in essence.

The key to our implementation of $m(\cdot)$ is the first author's graph isomorphism program nauty [22]. nauty takes a simple graph G, perhaps with colored atoms, and produces two outputs. One is the automorphism group Aut(G), in the form of a set of generators. The other is a canonical labeling of G, which is a graph c(G) isomorphic to G. The function c is "canonical" in the sense that c(G) = c(G') for every graph G' isomorphic to G. To apply nauty to a diagram (V, E), we can use the incidence graph $G = (V \cup E, \{(v, e) | v \in e\})$.

The generators for Aut(G) can be easily converted into generators for Aut(D) and then used to determine the equivalence classes of extensions. This enables us to implement the requirement of avoiding equivalent extensions.

The canonical labeling $c(\cdot)$ produced by nauty enables us to define $m(\cdot)$. Take the block *e* such that $D - e \in \mathcal{C}$ and *e* is given the least new label by $c(\cdot)$. If we define m(D) to be the orbit of blocks that contains *e*, we find that the three requirements we imposed on $m(\cdot)$ are satisfied.

As we have said, our real program uses a more complex definition of $m(\cdot)$. We do not use the incidence graph G, but instead use a prototype variant of nauty that operates on diagrams directly. Since our program makes connected diagrams, we took m(D) to be an orbit of feet if there were any, where a foot is a block with only one atom that also lies in other blocks. This avoids many connectivity tests, since removal of a foot necessarily preserves connectivity. It also avoids many futile extensions: adding a nonfoot e must be done in such a way that any existing feet become nonfeet, as otherwise $e \notin m(D + e)$.

In order to generate only the connected Greechie-3-L diagrams having M blocks, but no feet, a reasonable approach is to generate all the diagrams,

having feet or not, with M - 1 blocks first. Then the *M*th block can be added in such a way that uses at least two of the existing atoms and also turns any feet into nonfeet. It is also possible to make a generator that makes foot-free diagrams while staying entirely within that class, but it does not appear likely to be much different in efficiency.

Program greechie. Our implementation of the algorithm is a selfcontained program called greechie⁴ that takes as parameters the number of blocks, an optional upper bound on the number of atoms, and whether or not feet are permitted. It then produces one representative of each isomorphism class of connected Greechie-3-L diagram with those properties. The diagrams can then be processed as they are generated, with no need to store them. There is also an option for dividing the set of diagrams into disjoint subsets and efficiently producing only one of the subsets. This allows long computations to be broken into manageable pieces that can be run independently, even on different computers, without much change to the total running time.

In Table I we list the number of Greechie-3-L diagrams for small values of α and β . In each cell of the table, the upper value is the total number of connected Greechie-3-L diagrams and the lower value is the number of those which have no feet. Both counts are 0 if the table cell is empty. The table includes all possible values of β for $\alpha \leq 29$ and all possible values of α for $\beta \leq 17$.

For reasons explained later, we have particular interest in those diagrams containing close to the maximum number of blocks for a given number of atoms. This prompted us to compute additional near-maximal diagrams past the size where finding all the diagrams is practical.

To keep the discussion simple, we restrict ourselves to Greechie-3-L diagrams, not necessarily connected. By the *type* of a diagram we mean the pair (α , β), where α is the number of atoms and β is the number of blocks. The *rank* of an atom is the number of blocks which contain it.

The first observation is that a diagram *D* of type (α , β) has an atom whose rank is at most $\lfloor 3\beta/\alpha \rfloor$. Over most of our computational range, $\alpha < \beta$, so this value is at most 2. If there is an atom of rank 1, we can make *D* either by adding a foot to a diagram of type ($\alpha - 2$, $\beta - 1$) or by adding one block and one atom to a diagram of type ($\alpha - 1$, $\beta - 1$). On the other hand, if there is an atom of rank 2 but none of rank 1, we can make *D* by adding one atom and two blocks to a diagram of type ($\alpha - 1$, $\beta - 2$). So, if we have already made the diagrams of types ($\alpha - 2$, $\beta - 1$), ($\alpha - 1$,

⁴ftp://m3k.grad.hr/pavicic/greechie, http://cs.anu.edu.au/⁻bdm/ nauty/greechie.html. Many of the diagrams computed with the program are also available at those sites.

α\β	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Total
3	1														1
	1														1
5		0													0
_		1													1
7			0												0
0			2	0											2
9				0											0
10				4	1										4
10					1										1
11					0										0
					8										8
12					Ū	1									1
						3									3
13						0	2								2
						19	2								21
14							1	1							2
							14	1							15
15							0	6	1	1					8
							48	16	1	1					66
16								1	12	1					14
								62	15	11					78
17								0	11	21	2				34
10								126	119	24	3				272
18									1	71	27	4			103
10									281	209	31	4	6		525
19									255	19 810	201	67 84	0		333
20									555	019	490 251	04 834	147	1	1734
20										1230	231	1217	147	1	4970
Total	1	0	0	0	1	1	3	8	25	114	571	3675	27687	239644	7710
	1	1	2	4	9	22	64	205	771	3330	16571	95327	628555	4713887	

 Table I.
 Counts of Connected Greechie-3-L Diagrams

 β – 1), and (α – 1, β – 2), we can easily extend them to make those of type (α , β), provided $\alpha < \beta$.

Sometimes the class $(\alpha - 1, \beta - 2)$ maybe too difficult to compute. In this case, there are two other approaches we might be able to take to making those diagrams of type (α, β) whose minimum atom rank is 2. Define α_i to be the number of atoms of rank *i* (and recall that we are assuming $\alpha_1 = 0$). Counting the pairs (block, atom in block) in two ways, we have $2\alpha_2 + 3\alpha_3 + 4(\alpha - \alpha_2 - \alpha_3) \le 3\beta$. Since also $\alpha_2 + \alpha_3 \le \alpha$, we have

$$2\alpha_2 + \alpha_3 \ge 4\alpha - 3\beta$$
 and $\alpha_2 \ge 3\alpha - 3\beta$ (1)

Now consider the case of a Greechie-3-L diagram with $\alpha_1 = 0$ and

					Table I.	Continue	ed		
α\β	10	11	12	13	14	15	16	17	Total
21	0	29	2052	2884	405	4			5374
	1037	5199	8273	3872	437	4			18822
22		1	675	12849	10885	905	4		25319
		5377	22219	31805	13440	908	4		73753
23		0	42	10235	74698	45905	2837	17	133734
		3124	30923	109467	141454	53075	2861	17	340921
24			1	1508	113376	435636	207767	10723	769050
			22931	182581	588877	695281	225862	10756	1726327
25			0	57	37406	1061800	2670655	1030296	4849700
			9676	173841	1194496	3508197	3787093	1091395	9814445
26				1	2997	664386	9287661	17387077	33292775
				96213	1346088	8429567	22797227	22548822	61419172
27				0	75	110376	9494774	80621116	248439348
				30604	932262	11227170	64431928	161294913	423756705
28					1	5463	2905751	120414885	2009490610
					398359	9117775	99744819	530135665	3203436511
29					0	95	280150	60035427	17618049369
					98473	4807157	94035090	945730356	26495317580
30						1	9329	10280997	?
						1630602	57743948	1021031346	
31						0	118	636294	?
						321572	23993678	719130759	
32							1	15096	?
							6612148	346356783	
33							0	143	?
							1063146	116542189	
34								1	?
								26603735	
35								0	?
								3552563	
Total	114	571	3675	27687	229844	2324571	24859047	290432072	
	3330	16571	95327	626565	4713887	39791306	374437794	3894029319	

Table I. Continued

 $6\alpha > 7\beta$. Applying the second part of (1), we find that $2\alpha_2 > \beta$, which implies that some block contains at least two atoms of rank 2. Therefore, we can make the diagram by adding two atoms and three blocks to a diagram of type ($\alpha - 2$, $\beta - 3$).

If $6\alpha \le 7\beta$, but $\alpha > \beta$, there might be no atoms of rank 1, nor two atoms of rank 2 in the same block. In this case, we know that there are exactly $2\alpha_2$ blocks containing an atom of rank 2. The total rank of the atoms of rank greater than 3 is $3\beta - 2\alpha_2 - 3\alpha_3$, so we have at least $4\alpha_2 - (3\beta - 2\alpha_2 - 3\alpha_3) = 6\alpha_2 + 3\alpha_3 - 3\beta$ pairs (x, y) such that x and y are atoms of rank 2 and 3, respectively, lying in the same block. Now, if we suppose that

α\β	18	19	20	21	22	23	24	Total				
24	39							769050				
	39							1726327				
25	49350	134	2					4849700				
	49611	134	2					9814445				
26	5702603	247469	581					33292775				
	5952608	248066	581					61419172				
27	121218048	35519085	1471094	4180				248439348				
	147629428	36732179	1474036	4185				423756705				
28	717349032	911521449	247041933	10216096	35992	8		2009490610				
	1237468066	1062867580	253438446	10229781	35992	8		3203436511				
29	1448334695	6661929716	7447274324	1916771296	82563871	359550	245	17618049369				
	4690081789	10298834720	8422315646	1956383755	82670819	359550	245	26495317590				

Table I. Continued

 $14\alpha > 15\beta$, we find that $6\alpha_2 + 3\alpha_3 - 3\beta > \alpha_3$, implying that two of the pairs (*x*, *y*) have the same *y*. That is, there is an atom of rank 3 lying in two blocks which each contains an atom of rank 2. Therefore, we can make this diagram by adding three atoms and five blocks to a diagram of type ($\alpha - 3, \beta - 5$).

Using these ideas, we were able to compute (see Table II) the Greechie-3-L diagrams with up to 36 atoms having the maximum and, in some cases, near-maximum possible numbers of blocks.

The maximum-number of blocks shown in each row of the table is the maximum possible. All the diagrams counted in the table turned out to be connected, even though our programs did not assume connectivity.

Of some interest is that the Greechie-3-L diagrams of type (35, 35) and (36, 36) contain only atoms of rank 3. There is a connection here to known results in graph theory, as follows. Let *D* be a diagram in one of these two classes. Define a graph *G* whose vertices are the atoms and blocks of *D*. A

α\β	25	26	27	28	29	30	31	32	33	34	35	36
30	3982	4										
31	?	81068	71	1								
32	?	?	≥313813	1643								
33	?	?	?	?	≥51643	66						
34	?	?	?	?	?	≥185733	2113	19				
35	?	?	?	?	?	?	?	≥70035	325	17	5	
36	?	?	?	?	?	?	?	?	?	≥7871	136	1
37	?	?	?	?	?	?	?	?	?	?	?	≥1693

Table II. Counts of Large Extremal Greechie-3-L Diagrams

vertex which is an atom is adjacent in G to a vertex which is a block provided the atom lies in the block and there are no other edges. (This is the same incidence graph we defined earlier.) Then each vertex of the graph has valence 3, and moreover there are no cycles of length 9 or less (which is equivalent to the requirement that D has no loops of length 4 or less). In graph-theoretic language, G is a bipartite cubic (or trivalent) graph of girth at least 10. The smallest such graphs are three with 70 vertices [23]. In two of the three graphs, the functions of "atom" and "block" can be interchanged to produce different Greechie diagrams, but in the third this interchange gives an isomorphic diagram. That is how the three graphs correspond to the five diagrams we found.

Figure 2 shows the unique Greechie-3-L diagram of type (36, 36). Using a slight adaptation of the program described in ref. 24, we have found that



Fig. 2. The unique Greechie-3-L diagram of type (36, 36).

there are no Greechie-3-L diagrams of type (37, 37) with every atom having rank 3, and exactly eight such diagrams of type (38, 38).

3. TESTING CONJECTURES WITH GREECHIE DIAGRAMS

In order to test equations conjectured to hold in various classes of orthomodular lattices, it is useful to automate their checking against Greechie diagrams. This will let us either falsify the conjecture or give us some confidence that might hold in the class of interest before we attempt to prove it. Also, finding a lattice in which one equation holds, but a second one fails gives us a proof that the second is independent.

To this end we use a program called latticeg,⁵ which will check to see if an equation or inference holds in each of the Greechie diagrams in a list provided by the user.

The latticeg program internally converts a Greechie diagram to its corresponding Hasse diagram and tests all possible assignments of nodes in the Hasse diagram to the equation. In general Greechie diagrams correspond to Boolean algebras "pasted" together.

The Hasse diagrams for the Boolean algebras corresponding to two-, three-, and four-atom blocks are shown in Fig. 3. The Greechie diagram for a given lattice may be drawn in several equivalent ways: Figure 4 shows the same Greechie diagram drawn in two different ways, along with the corresponding Hasse diagram. From the definitions we see that the ordering of the atoms on a block does not matter, and we may also draw blocks using arcs as well as straight lines as long as the blocks remain clearly distinguishable.

Recall that a *poset* (partially ordered set) is a set with an associated ordering relation that is reflexive $(a \le a)$, antisymmetric $(a \le b, b \le a \text{ imply } a = b)$, and transitive $(a \le b, b \le c \text{ imply } a \le c)$. An *orthoposet* is a poset with lower and upper bounds 0 and 1 and an operation ' satisfying (i) if $a \le b$, then $b' \le a'$; (ii) a'' = a; and (iii) the infimum $a \cap a'$ and the supremum $a \cup a'$ exist and are 0 and 1, respectively. A *lattice* is a poset in which any two elements have an infimum and a supremum. An orthoposet is *orthomodular* if $a \le b$ implies (i) the supremum $a \cup b'$ exists and (ii) $a \cup (a' \cap b) = b$. A lattice is *orthomodular* if it is also an orthomodular

⁵ Available at ftp://ftp.shore.net/members/ndm/quantum-logic as the ANSI C program latticeg.c. The program is simple to use and self-explanatory with the -help option. A related program lattice.c handles general Hasse diagrams and has built-in those lattices we have found most useful for preliminary testing of conjectures. These two programs, along with beran.c for computing the canonical orthomodular form of any two-variable expression, are the primary computational tools we have used for studying orthomodular and Hilbert lattice equations.

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Fig. 3. Greechie diagrams for Boolean lattices 2^2 , 2^3 , and 2^4 labeled with the atoms of their corresponding Hasses diagrams shown above them. (2^4 was adapted from ref. 25, Fig. 18, p. 84).

poset. For example, Boolean algebras such as those of Fig. 3 are orthomodular lattices. A σ -orthomodular poset is an orthomodular poset in which every countable subset of elements has a supremum. An *atom* of an orthoposet is an element $a \neq 0$ such that b < a implies b = 0.

In the literature, there are several different definitions of a Greechie diagram. For example, Beran [25, p. 144] forbids two-atom blocks. Kalmbach [17, p. 42] as well as Pták and Pulmannová [21, p. 32] include all diagrams with two-atom blocks connected to other blocks as long as the resulting pasting corresponds to an orthoposet. However, the case of two-atom blocks connected to other blocks is somewhat complicated; for example, the definition of a loop in Definition 2.1 must be modified (e.g., ref. 17, p. 42) and



Fig. 4. Two different ways of drawing the same Greechie diagram, and its corresponding Hasse diagram.

no longer corresponds to the simple geometry of a drawing of the diagram. The definition of a Greechie diagram also becomes more complicated; for example a pentagon (or any *n*-gon with an odd number of sides) made out of two-atom blocks is not a Greechie diagram (i.e., does not correspond to any orthoposet).

The definition of Svozil and Tkadlec [20] that we adopt, Definition 2.2, excludes two-atom blocks connected to other blocks. It turns out that all orthomodular posets representable by Kalmbach's definition can be represented with the diagrams allowed by Svozil and Tkadlec's definition. But the latter definition eliminates the special treatment of two-atom blocks connected to other blocks and in particular simplifies any computer program designed to process Greechie diagrams.

Svozil and Tkadlec's definition further restricts Greechie diagrams to those diagrams representing orthoposets that are orthomodular by forbidding loops of order less than 4, unlike the definitions of Beran and Kalmbach. The advantage appears to be mainly for convenience, as we obtain only those Greechie diagrams that correspond to what are sometimes called "quantum logics" (σ -orthomodular posets). (We note that the term "quantum logic" is also used to denote a propositional calculus based on orthomodular or weakly orthomodular lattices [26].)

The definition allows for Greechie diagrams whose blocks are not connected. In Fig. 5 we show the Greechie diagram for the *Chinese lantern* MO2 using unconnected two-atom blocks. This example also illustrates that even when the blocks are unconnected, the properties of the resulting orthoposet are not just a simple combination of the properties of their components (as one might naively suppose) because we are adding disjoint sets of incomparable nodes to the orthoposet. As is well known [17, p. 16], MO2 is not distributive, unlike the Boolean blocks from which it is built.

The latticeg program takes, as its inputs, a Greechie diagram ASCII representation (or more precisely a collection of them) and an equation (or inference) to be tested. This ASCII representation is compatible with the output of the programs described in Section 2. Currently latticeg is



Fig. 5. Greechie diagram for the lattice MO2 and its Hasse diagram. The dashed line indicates that the unconnected blocks belong to the same Greechie diagram.

designed to work only with Greechie diagrams corresponding to lattices, i.e., that have no loops of order 4 or less, as these are the most interesting for studying equations valid in all Hilbert lattices. It converts the Greechie diagram to its corresponding Hasse diagram (internally stored as truth tables). Finally, the program tests all possible assignments of nodes in the Hasse diagram to the input equation under test.

The latticeg program also incorporates classical propositional metalogic and predicate calculus to allow the study of such characteristics as atomicity and superposition.

As a simple example of the operation of the latticeg program, we show how it verifies the passage and failure of the modular law [Eq. (11) below] on the lattices of Figs. 7b and 7c. We create a file with a name such as m.gre to represent the lattices, containing the lines

```
123, 345.
123, 345, 567.
```

and run the program by typing

latticeg -i m.gre "(av(b^(avc)))=((avb)^(avc))"

The program responds with

```
The input file has 2 lattices.
Passed #1 (5/2/12)
FAILED #2 (7/3/16) at (av(f^(avb)))=((avf)^(avb))
```

The notation should be more or less apparent, but is described in detail by the program's help. The numbers "5/2/12" show the atom/block/node count, and the failure shows the internal Hasse diagram's nodal assignment to the equation's variables.

Let us consider an application of the program. Closed subspaces \mathcal{H}_a , \mathcal{H}_b of any infinite-dimensional Hilbert space \mathcal{H} form a lattice in which the operations are defined in the following way: $a' = \mathcal{H}_a^{\perp}$, $a \cap b = \mathcal{H}_a \cap \mathcal{H}_b$, and $a \cup b = (\mathcal{H}_a + \mathcal{H}_b)^{\perp \perp}$. In such a lattice its elements satisfy the following condition, i.e., in any infinite-dimensional Hilbert space its closed subspaces satisfy the following equation, which is called the *orthoarguesian equation*:

$$a \perp b \& c \perp d \& e \perp f$$

$$\Rightarrow (a \cup b) \cap (c \cup d) \cap (e \cup f)$$

$$\leq b \cup (a \cap (c \cup (((a \cup c) \cap (b \cup d))))$$

$$\cap (((a \cup e) \cap (b \cup f)) \cup ((c \cup e) \cap (d \cup f)))))) (2)$$

where $a \perp b$ means $a \leq b'$.

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We wanted, first, to reduce the number of variables in this equation and, second, to generalize the equation to n variables. In attacking the first problem the program helped us quickly to eliminate dead ends: a failure of a conjectured equation in a lattice in which Eq. (2) held meant that the latter equation was weaker and vice versa. Thus we arrived at the following 4-variable equation, which we call the 4OA law:

$$(a_1 \to_1 a_3) \cap (a_1 \stackrel{(4)}{\equiv} a_2) \le a_2 \to_1 a_3 \tag{3}$$

where the operation $\stackrel{(4)}{\equiv}$ is defined as follows:

$$a_1 \stackrel{(4)}{=} a_2 \stackrel{\text{def}}{=} (a_1 \stackrel{(3)}{=} a_2) \cup ((a_1 \stackrel{(3)}{=} a_4) \cap (a_2 \stackrel{(3)}{=} a_4))$$
(4)

where

$$a_1 \stackrel{(3)}{\equiv} a_2 \stackrel{\text{def}}{\equiv} ((a_1 \to a_3) \cap (a_2 \to a_3)) \cup ((a_1' \to a_3) \cap (a_2' \to a_3))$$
(5)

where $a \rightarrow_1 b \stackrel{\text{def}}{=} a' \cup (a \cap b)$. We then proved "by hand" that Eqs. (2) and (3) are equivalent [19]. We also proved that the following generalization (which we call the *n*OA law) of Eq. (3)

$$(a_1 \to_1 a_3) \cap (a_1 \stackrel{(n)}{\equiv} a_2) \le a_2 \to_1 a_3 \tag{6}$$

where

$$a_1 \stackrel{(n)}{\equiv} a_2 \stackrel{\text{def}}{\equiv} (a_1 \stackrel{(n-1)}{\equiv} a_2) \cup ((a_1 \stackrel{(n-1)}{\equiv} a_n) \cap (a_2 \stackrel{(n-1)}{\equiv} a_n)), \qquad n \ge 4$$
(7)

holds in any Hilbert lattice. To show that this generalization is a nontrivial one, the program is all we need, we need not prove anything "by hand." It suffices to find a Greechie diagram in which the 4OA law holds and 5OA law fails. An 800-MHz PC took a few days to find such a lattice (shown in Fig. 6) [19], while to find it "by hand" is, due to the number of variables



Fig. 6. Greechie diagram for OML L46.

in the equation and nodes in the corresponding Hasse diagram, apparently humanly impossible.

Considerable effort was put into making the program run fast, with methods such as exiting an evaluation early when a hypothesis of an inference fails. Truth tables for all built-in compound operations (such as the various quantum implications and the quantum biconditional) are precomputed. The innermost loop (which evaluates an assignment) was optimized for the fastest runtime we could achieve. The number of assignments of lattice nodes to equation variables that must be tested is n^{ν} , where *n* is the number of nodes in the Hasse diagram and v is the number of variables in the equation. The algorithm requires a time approximately proportional to kn^{ν} , where k is the length of the equation expressed in Polish notation. For a typical equation (k = 20) with no hypotheses, the algorithm currently evaluates around 1 million assignments per second on an 800-MHz PC. The speed is typically faster when hypotheses are present, particularly if they have fewer variables than the conclusion. If a lattice violates an equation, it often happens (with luck) that the first failure will be found quickly, in which case further evaluations do not have to be done.

The propositional metalogic feature of latticeg, when carefully used in a series of hypotheses with a successively increasing number of variables, can sometimes be exploited to achieve orders of magnitude speedup with certain equations containing many variables. For example, we tested the eight-variable Godowaki equation against a 42-node lattice in 16 hr, whereas without the speedup it would have required around 10^5 hr. To illustrate how this speedup works, we can add to the four variable Godowski equation [Eq. (9) below] hypotheses as follows:

$$\sim (d \to_1 a \le a \to_1 d) \& \sim ((c \to_1 d) \cap (d \to_1 a) \le a \to_1 d)$$
$$\Rightarrow (a \to_1 b) \cap (b \to_1 c) \cap (c \to_1 d) \cap (d \to_1 a) \le a \to_1 d$$
(8)

Here \sim means metalogical NOT. The hypotheses are redundant in any ortholattice, which is the case for the Greechie diagrams that are of interest to us. We take advantage of the latticeg feature that exits the evaluation of a lattice nodal assignment if a hypothesis fails. If an assignment to the first hypothesis, with only two variables, fails (as it typically does for some assignments) it means we do not have to scan the remaining variables. Similarly, if the first hypothesis passes, but the second (with three variables) fails, we can skip the evaluation of the four-variable conclusion.

We are currently exploring the exploitation of possible symmetries inherent in the Greechie diagram to speed up the program further, but we have not yet achieved any results in this direction. For certain special cases such as the Godowski equations we are also exploring the use of a "dynamic programming" technique that may provide a runtime proportional to kn^4 instead of kn^{ν} , regardless of the number of variables.

For fastest runtime, it is desirable to screen equations with the smallest Greechie diagrams first. For this purpose what matters is the size of the Hasse diagram and not the Greechie diagram. In a chain of blocks each having two atoms connected, a three-atom block adds four nodes to the Hasse diagram, whereas a four-atom block adds 12 nodes. For example, the decagon (10 blocks) has 42 nodes with three-atom blocks and 122 nodes with four-atom blocks. With three-atom blocks, a six-variable equation—our practical upper limit when there are no strong hypotheses—must be evaluated $42^6 = 5.5$ billion times (a few hours of CPU time on an 800-MHz PC), but with four-atom blocks it would take $122^6 = 3.3$ trillion evaluations, which is currently impractical. So far most of our work has been done using diagrams with every block having size 3.

Two other heuristics have helped us to falsify conjectures more quickly. The first is to first scan Greechie diagrams with the highest block-to-atom ratio (Table I). Such diagrams seem to have the most complex "structure" with the most likelihood of violating a nonorthomodular equation ("nonor-thomodular equation" here means an equation which turns the orthomodular lattice variety into a smaller one when added to it). A drawback is that at higher atom counts, virtually every such diagram violates almost any nonorthomodular equation, making it very useful for identifying nonorthomodular properties, but less useful for proving independence results. For example, for 35 and 36 atoms (the case we elaborated in Section 2) the highest ratio is 1 (Table II) and in those diagrams all equations that we know to be nonorthomodular fail. Hence, for example, 36×36 (36 atoms, 36 blocks) is a very useful tool for an initial scanning of equations we want to check for "nonorthomodularity."

The second heuristic is our empirical observation that Greechie diagrams without feet often behave in the same way as the same diagram with feet added. For example, the Peterson OML (Fig. 7a), with 32 nodes, is the smallest lattice that violates Godowski's four-variable strong state law [19], which holds in any infinite-dimensional Hilbert space:

$$(a \to_1 b) \cap (b \to_1 c) \cap (c \to_1 d) \cap (d \to_1 a) \le a \to_1 d \tag{9}$$

but not Godowski's three-variable law (which also holds in infinite-dimensional Hilbert spaces)

$$(a \to_1 b) \cap (b \to_1 c) \cap (c \to_1 a) \le a \to_1 c \tag{10}$$

The Peterson OML is useful as a test for an equation derived from the fourvariable law and conjectured to be equivalent to it: if it does not violate the conjectured equation, we know the equation is weaker than the four-variable



Fig. 7. (a) Peterson OML, (b) Greechie diagram obeying modular law, (c) Greechie diagram violating modular law.

law. Now, we observe empirically that we may add a foot (a three-atom block connected at only one point) to any of its 15 atoms without changing this behavior. We have also not seen a chain of feet or combination of feet that changes this behavior when added to the diagram.

So, by scanning only lattices without feet, we can obtain a speedup of 20 times for 14-block lattices (Table I). Supporting this heuristic is the fact that complex Greechie diagrams with feet are rarely found in the literature. We obtained an additional support by scanning the 4OA law given by Eq. (3) and Godowski's three-variable equation (10) through several million lattices with free feet versus those with feet stripped: we did not find a single difference. To our knowledge there is only one special case for which feet do make a difference. Figure 7b shows a lattice that obeys the modular law

$$a \cup (b \cap (a \cup c)) = (a \cup b) \cap (a \cup c) \tag{11}$$

but violates it when a foot is added (Fig. 7c). This special case might well be insignificant because, of all Greechie diagrams, only star-like ones (Fig. 7b, $D_{3,2}$ and $D_{4,4}$ from Fig. 1, etc.) are modular. As soon as we add any block to any other atom apart from the central one in such a lattice, we make it nonmodular.

Another heuristic for reducing the number of diagrams to be scanned is suggested by the following observation, although we have not implemented it. We can list the diagrams in such a way that (except for the first diagram) each is formed by adding one block to a diagram earlier in the list. Whenever the earlier diagram violates an equation, we have observed that it is very likely that the new diagram will also violate the equation. By skipping the new diagram in this case (presuming the probable violation), a speedup can be obtained.

However the above observation does not universally hold, i.e., sometimes the new diagram will pass an equation violated by the earlier one. An example is shown in Fig. 8. The diagram L38 violates the orthoarguesian law [Eq. (2)]. But if we extend L38 by adding two blocks as shown in Fig. 8b, the resulting diagram will pass not only this law [equivalent to 4OA given by Eq. (3)], but also 5OA and 6OA [given by Eq. (6) for n = 5 and n = 6, respectively].

If our various speedup heuristics are used for practical reasons, the user must be aware that a diagram scan may be incomplete. So if, in our example, the extended L38 (by passing) could serve to prove a certain independence result, it would be missed by the scan. Currently we have no data to indicate how often such cases would be missed.

Any scan of diagrams to test an equation is, of course, *a priori* incomplete since the number of diagrams is infinite. The various heuristics we have described may cause some lattices in any finite list to be skipped. But if a lattice with the desired properties is found more quickly, our goal is achieved. Of course a scan can be continued for any diagrams omitted by the heuristics if more completeness is desired.

4. CONCLUSIONS

Greechie diagrams (we used the version given by Definition 2.1, and discussed the others in Section 3) are generators of examples and counterexamples of orthomodular nonmodular lattices. They are special cases of lattices one obtains by using the recent generalization by Navara and Rogalewicz [27, 28] of Dichtl's pasting construction for orthomodular posets and lattices [29]. Navara and Rogalewicz's method exhaustively generates all finite orthomodular nonmodular lattices, but Greechie diagrams are apparently easier to generate and certainly much easier to test lattice equations with. For these reasons, Greechie diagrams have been used almost exclusively so far.



Fig. 8. Greechie diagrams for (a) L38 and (b) L38 with two blocks added.

Since any infinite-dimensional complex Hilbert space is orthoisomorphic to a Hilbert lattice which is orthomodular and nonmodular, Greechie diagrams represent an indispensable tool for Hilbert space investigation. This has also been prompted by recent developments in the field of quantum computing. However, as we stressed in the Introduction, the existing (both manual and automated) constructions of Greechie diagrams and their application to Hilbert space properties (which resulted in many important results at the time) recently reached the frontiers of human manageability. Therefore, in Section 2 we gave an algorithm and a program for generating Greechie lattices with theoretically unlimited numbers of atoms and blocks. The algorithm is of a completely different kind from the only earlier algorithm and the program is at least 10⁵ times faster. In Section 3 we then gave an algorithm and programs for automated checking of lattice properties on Greechie diagrams.

Our algorithm for generating Greechie diagrams, given by Definition 2.4, works by defining a unique construction path for each isomorphism class of diagrams. It enabled us to produce a self-contained program greechie for automated generation of diagrams with a specified number or range of atoms and/or blocks. Several properties and several special types of Greechie diagram construction are discussed in that section, as are connections to some equivalent results in graph theory.

The algorithm for automated checking of lattice properties described in Section 3 works by converting a Greechie diagram to its corresponding Hasse diagram and then converting the Hasse diagram to a truth table for supremum and orthocomplementation. It enabled us to construct a self-contained program latticeg which takes, as its inputs, Greechie diagrams in ASCII representation and an equation or inference or quantified expression to be tested. Many programming speedups have been used to make the program run as fast as possible. For example, all built-in compound operations are precomputed, many C code tricks are used, etc. Also, additionally to speed up scanning, we use many Greechie diagram heuristics that we found recently: a lattice equation is most likely to fail in a lattice with the highest block-to-atom ratio, scanning of an equation on a diagram with no feet and on the same diagram with feet added makes no difference for most diagrams, etc. Several properties and several special cases of Greechie lattices are given and discussed in Section 3. In particular, it is explained how we proved that our recent nvariable generalization of the orthoarguesian equation is a nontrivial one by using nothing but latticeg applied to an output of greechie.

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